**LIST OF PRACTICALS**

**2CE225 – COMPUTER ARCHITECTURE AND PERIPHERALS**

1. To implement an incrementer and decrementer on Logisim. {4 hours}

To implement a circuit to find the Greatest Common Divisor for 3 bit numbers using the Division Method and using counter.

1. To implement a Common Bus Transfer system for four 4 bit Register. {2 hours}
2. Design of Arithmetic Unit as per the definition given in Exercise No. 4.15 of the text book. {2 hours}
3. Design of Arithmetic and Logical Unit (Extension of Experiment No. 3) with following logical operations: {2 hours}
   1. AND
   2. OR
   3. XOR
   4. NOT
4. Extension of Experiment No. 4 with 4 shift operations – SHL, SHR, CIL, CIR {2 hours}
5. Simulation of Fetch Cycle {4 hours}
6. Simulation of 1 byte BCD Adder / Subtractor {4 hours}
7. Simulation of Multiplier using signed magnitude method. {2 hours}
8. Simulation of floating point adder / multiplier {4 hours}
9. Simulation of Priority Interrupt Hardware {4 hours}
10. Simulation of the Associative Memory {4 hours}
11. Simulation of the Priority Encoder {4 hours}
12. Simulation of Divider using the signed magnitude method. {2 hours}